

# Memory Module Specifications

## KF560C30BW-16

16GB 2G x 64-Bit

DDR5-6000 CL30 288-Pin DIMM



### DEFAULT SPECIFICATIONS

CL(IDD)	40 cycles
Row Cycle Time (tRCmin)	48ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	295ns(min.)
Row Active Time (tRASmin)	32ns(min.)
UL Rating	94 V - 0
Operating Temperature	0° C to +85° C
Storage Temperature	-55° C to +100° C

### DESCRIPTION

Kingston FURY KF560C30BW-16 is a 2G x 64-bit (16GB) DDR5-6000 CL30 SDRAM (Synchronous DRAM) 1Rx8, memory module, based on eight 2G x 8-bit FBGA components per module. The module supports Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Each module has been tested to run at DDR5-6000 at a low latency timing of 30-36-36 at 1.4V. The SPDs are programmed to JEDEC standard latency DDR5-4800 timing of 40-39-39 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

### DEFAULT FEATURES

- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- Height 1.37" (34.9mm), w/heatsink

### FACTORY TIMING PARAMETERS

- Default (JEDEC): DDR5-4800 CL40-39-39 @1.1V
- XMP Profile #1: DDR5-6000 CL30-36-36 @1.4V
- XMP Profile #2: DDR5-5600 CL40-40-40 @1.25V
- XMP Profile #3: DDR5-4800 CL38-38-38 @1.1V

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## MODULE WITH HEAT SPREADER



## MODULE DIMENSIONS



All measurements are in millimeters.  
(Tolerances on all dimensions are  $\pm 0.15$  unless otherwise specified)



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Revision No.	History	Release Date	Remark	Editor	Approved
A	Initial Release	02/15/24		David Y.	Alex S.
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